

Xiaoyang Guo

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EDUCATION	The Chinese University of Hong Kong , Hong Kong SAR	
	<ul style="list-style-type: none">Ph.D. Student in Electronic EngineeringSupervised by Prof. Xiaogang Wang & Prof. Hongsheng LiFocus on deep learning and computer visionAwardee of Hong Kong PhD Fellowship Scheme (HKPFS)	Aug 2017 – Present
	Tsinghua University , Beijing, China	
	<ul style="list-style-type: none">B.Eng. in Computer Science and TechnologyGPA: 92 / 100, Rank: 5 / 107	Aug 2013 – Jul 2017
RESEARCH INTERESTS	<ul style="list-style-type: none">3D Vision: 3D Geometry, Multi-view Geometry, Depth Estimation, SLAMLow-level Computer Vision: Optical Flow, Stereo Matching	
ACADEMIC EXPERIENCE	National Tsing Hua University , Hsinchu, Taiwan	
	<ul style="list-style-type: none">Parallel Computing Summer SchoolTaught by Prof. Yeh-Ching Chung. Focus on MPI and CUDA	Jul 2015
	Carnegie Mellon University , Pittsburgh, USA	
	<ul style="list-style-type: none">Summer Research InternSupervised by Prof. Abhinav GuptaUtilize human pose trajectories to improve video action recognition accuracy.	Jul 2016 – Aug 2016
	SenseTime Group Limited , Beijing, China	
	<ul style="list-style-type: none">Research InternSupervised by Yi Sun and Junjie YanDesign neural network structures to improve face recognition algorithms.	Oct 2016 – Mar 2017
	The University of Western Australia , Perth, Australia	
	<ul style="list-style-type: none">Undergraduate Research on Parallel ComputingSupervised by Prof. Zihui Du (Tsinghua) and Prof. Linqing Wen (UWA)Optimize and reduce the latency of gravitational wave data analysis programs using CUDA.	Apr 2017 – Jun 2017
	SenseTime Group Limited , Beijing, China	
	<ul style="list-style-type: none">Research InternImprove the accuracy and the generalization ability of face anti-spoofing algorithms.Build up multi-spectral face anti-spoofing systems.	May 2018 – Sep 2018
AWARDS & SCHOLARSHIPS	<ul style="list-style-type: none">Scholarship for Academic Excellence - Beijing, ChinaNational Scholarship - Beijing, ChinaApac Tsinghua Ceo Cci Bhd Scholarship - Beijing, ChinaHong Kong PhD Fellowship - Hong Kong SAR	2014 2015 2016 2017
PUBLICATIONS	[1] Xiaoyang Guo , Kai Yang, Wukui Yang, Hongsheng Li, and Xiaogang Wang. Group-wise correlation stereo network. In <i>Conference on Computer Vision and Pattern Recognition (CVPR)</i> , 2019	
	<ul style="list-style-type: none">We propose a new operation called group-wise correlation to construct cost volumes for stereo matching, which provides better and more efficient similarity measures. The proposed method achieves better performance than state-of-the-art methods on KITTI.	

- [2] Mingyang Liang*, **Xiaoyang Guo***, Hongsheng Li, Xiaogang Wang, and You Song. Unsupervised cross-spectral stereo matching by learning to synthesize. In *33rd AAAI Conference on Artificial Intelligence (AAAI)*, 2019 **(Oral)**
 - We propose a novel unsupervised cross-spectral stereo matching framework. Appearance variations between multi-spectral images are minimized by a style adaptation network with cycle consistency and adversarial learning, which is end-to-end optimized with an unsupervised stereo matching network.
- [3] **Xiaoyang Guo**, Hongsheng Li, Shuai Yi, Jimmy Ren, and Xiaogang Wang. Learning monocular depth by distilling cross-domain stereo networks. In *Proceedings of the European Conference on Computer Vision (ECCV)*, 2018
 - A stereo matching network is utilized as a proxy to learn depth from large-scale synthetic data, which is then used to supervise monocular depth estimation networks. Experiments show state-of-the-art results of monocular depth estimation.
- [4] Hongyang Li, **Xiaoyang Guo**, Bo Dai, Wanli Ouyang, and Xiaogang Wang. Neural network encapsulation. In *Proceedings of the European Conference on Computer Vision (ECCV)*, 2018
 - We approximate the routing process in Capsule networks with a two-branch design. The complexity and runtime of the model are decreased by a large margin.

OTHER PROJECTS

THCO MIPS CPU in VHDL

2016

- This is the final group project for the course *Principles of Computer Organization*. A simplified pipelined MIPS processor is implemented with VHDL.

SKILLS

- Computer Languages: C, C++, CUDA, Python, HTML, JavaScript
- Softwares & Platforms: Proficient in Linux, PyTorch